

FIG. 1 A

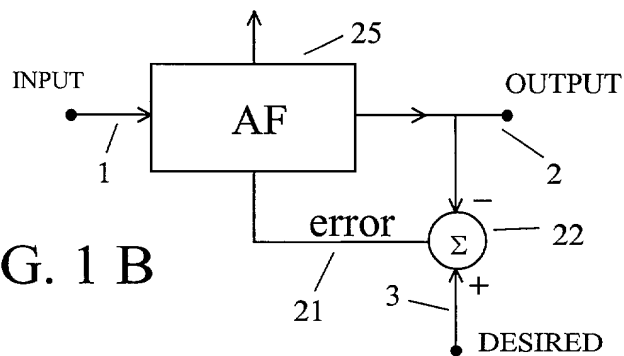


FIG. 1 B

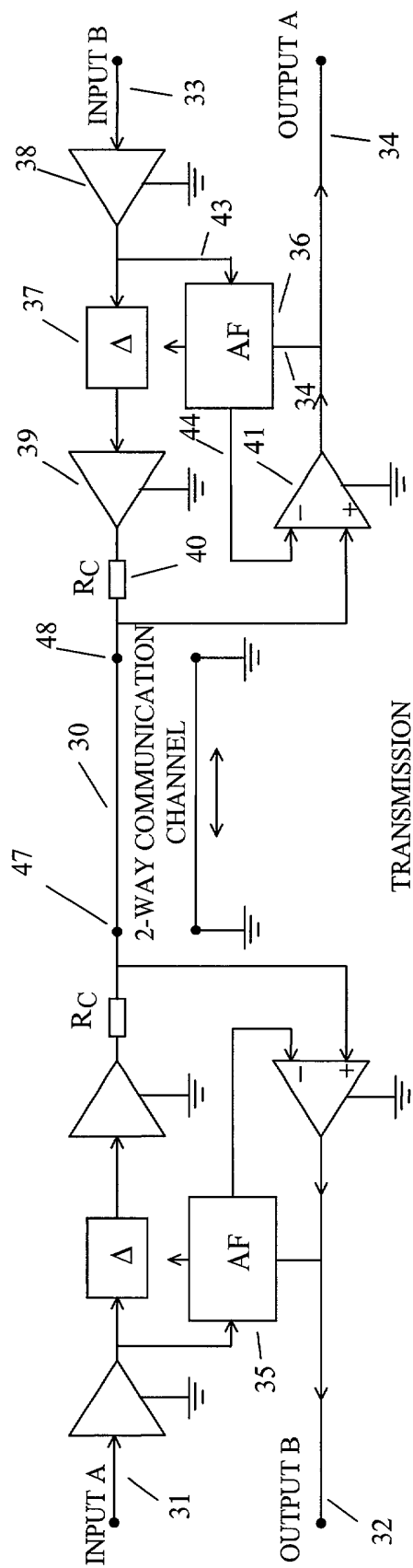


FIG. 2 A

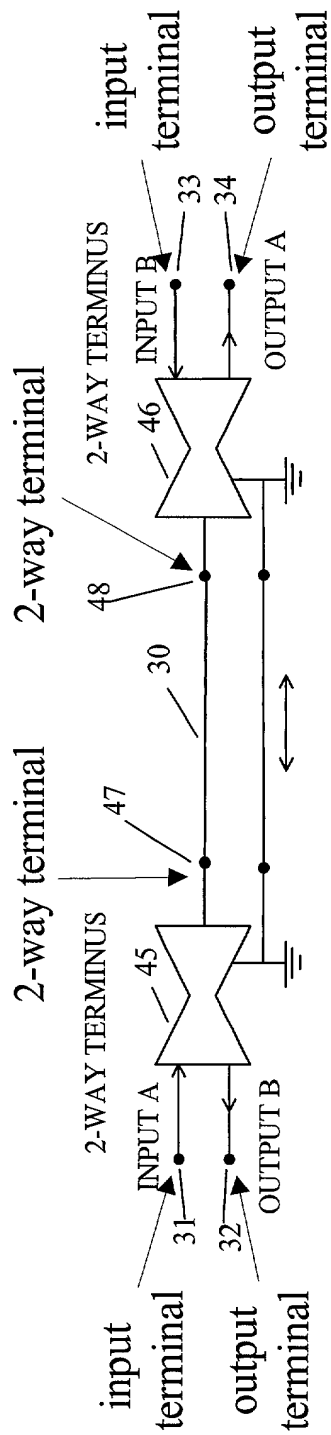


FIG. 2 B

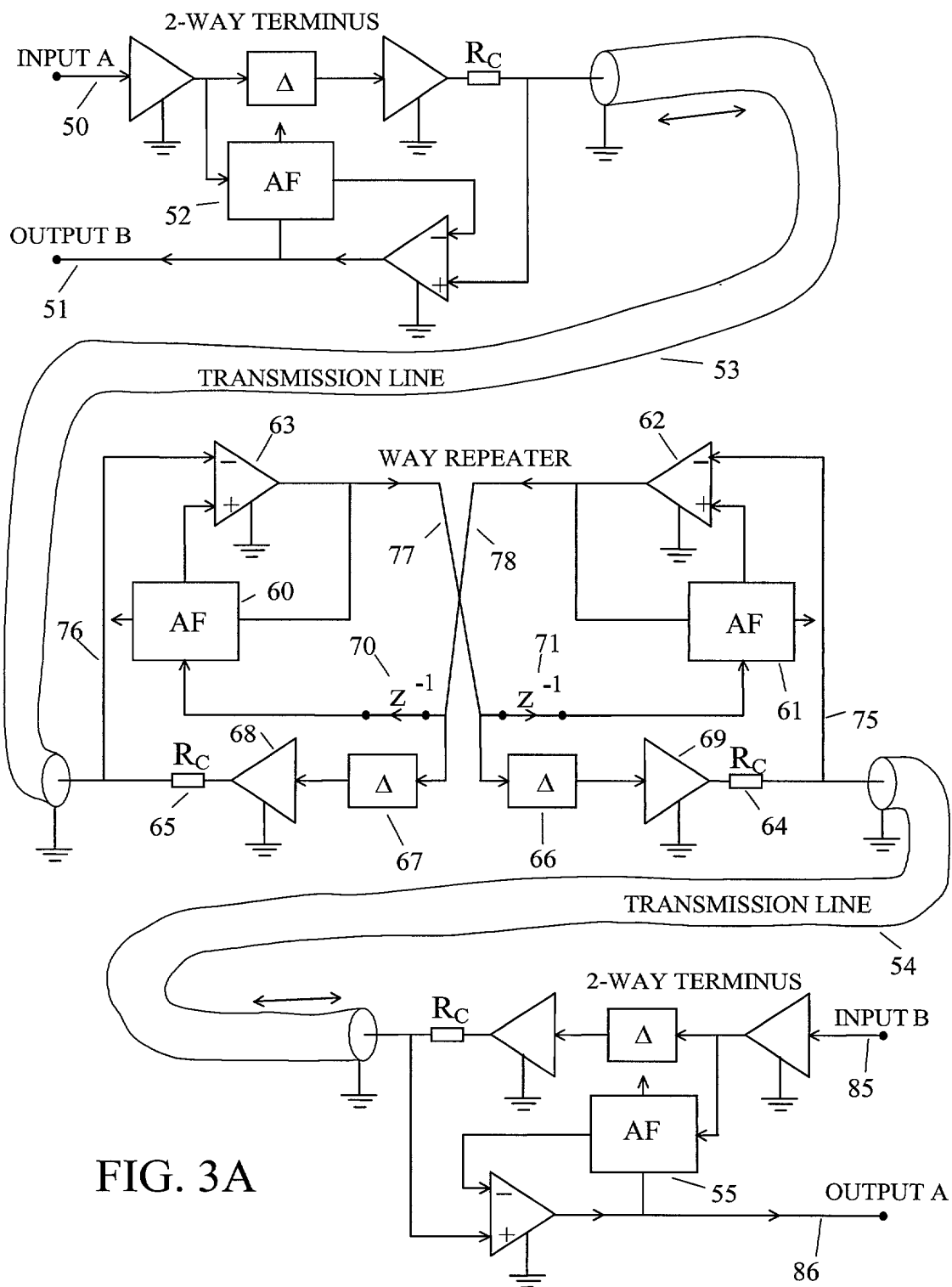


FIG. 3A

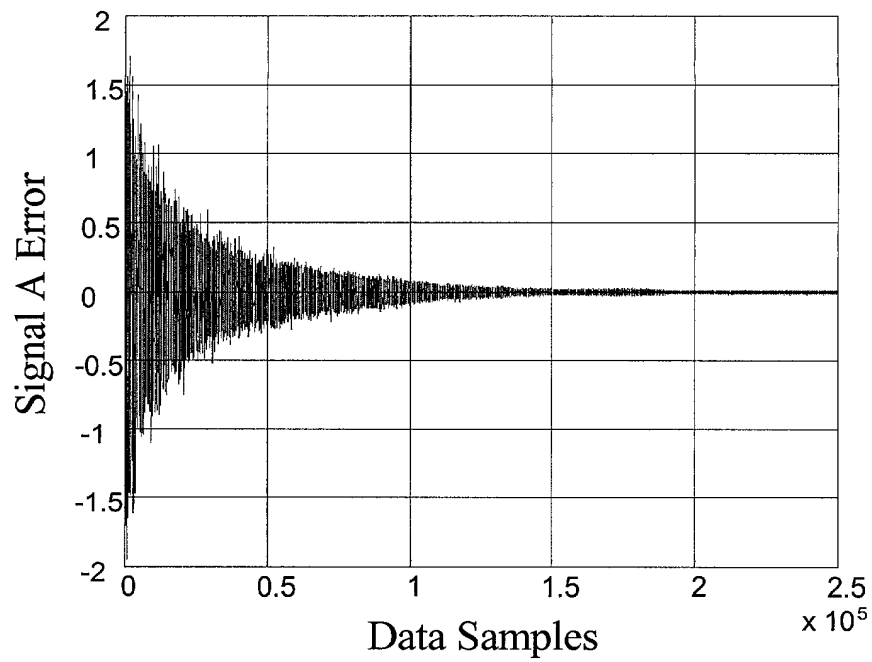


FIG. 3 B

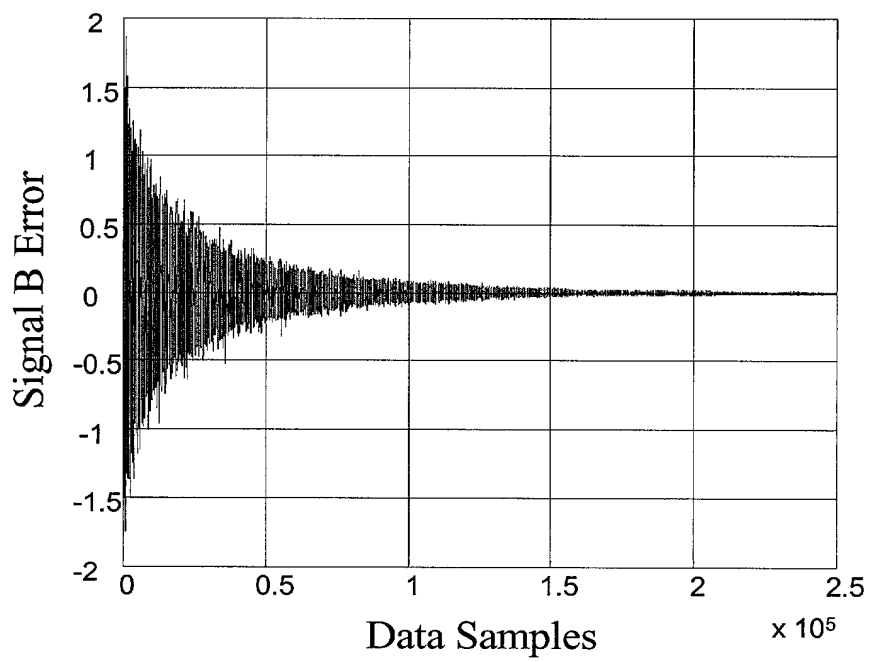


FIG. 3 C

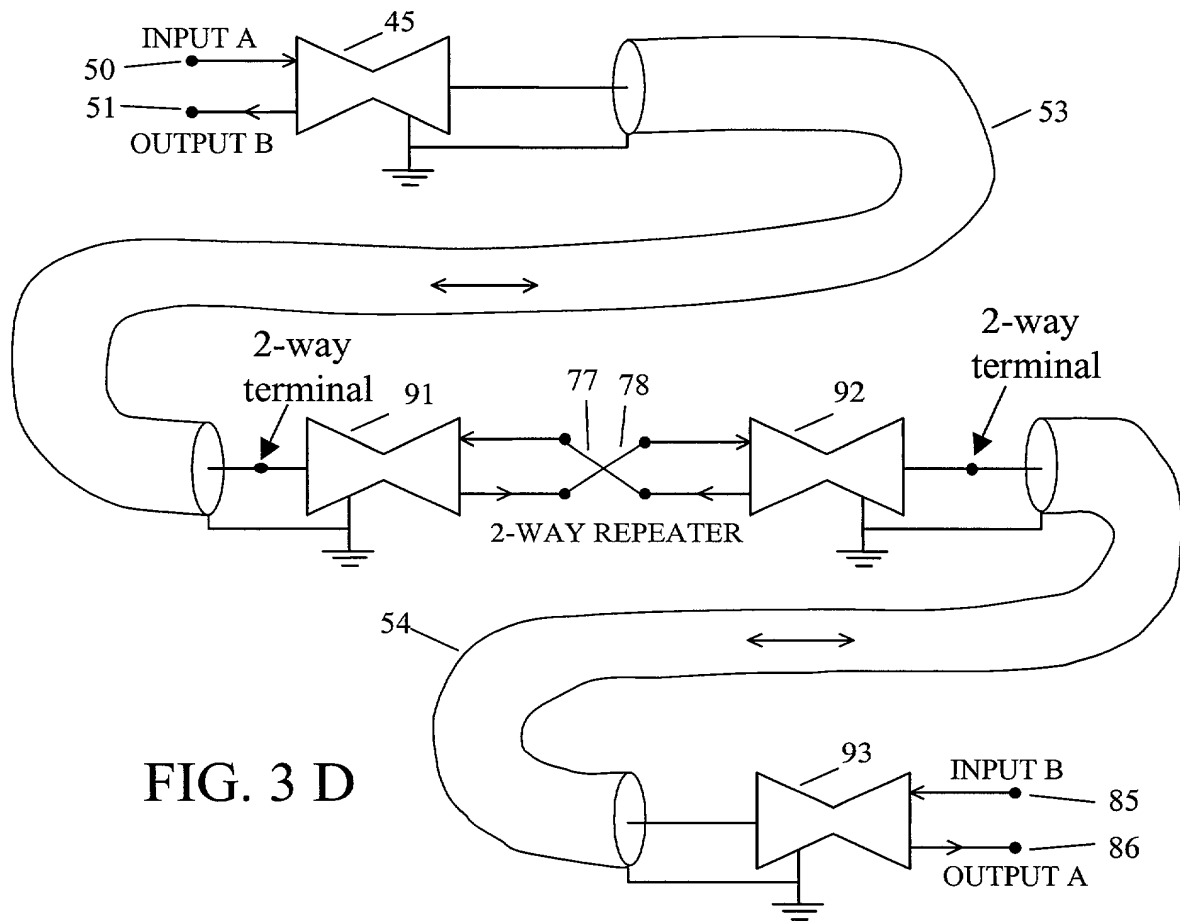


FIG. 3 D

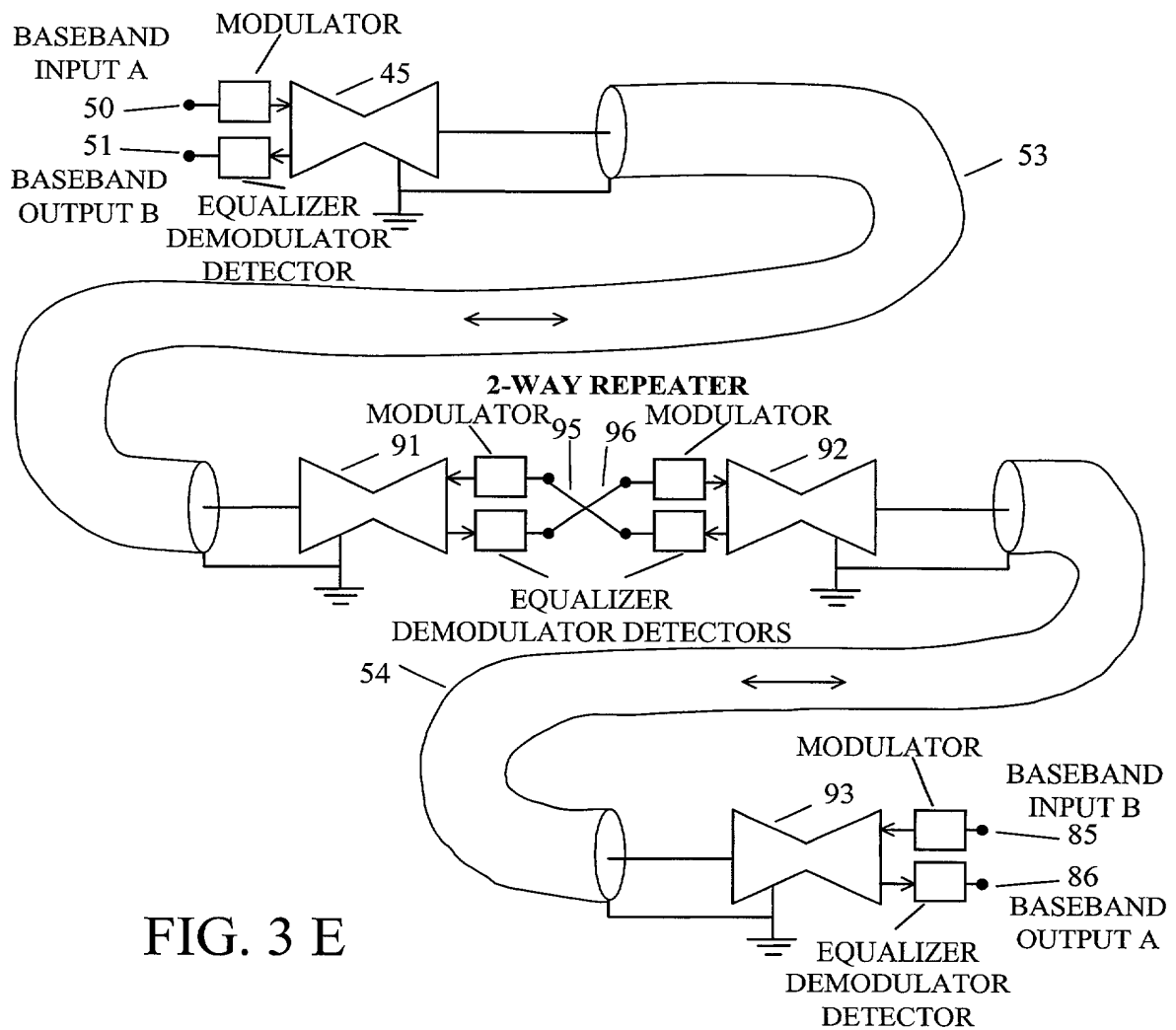
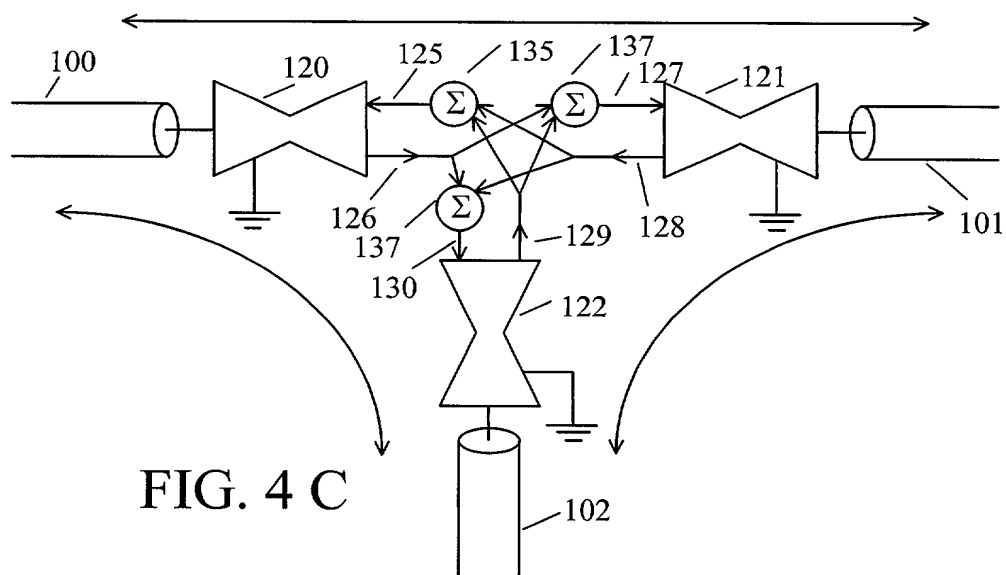
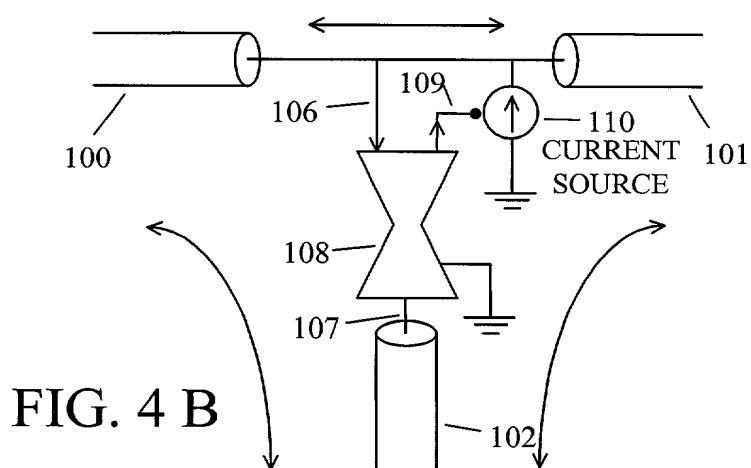
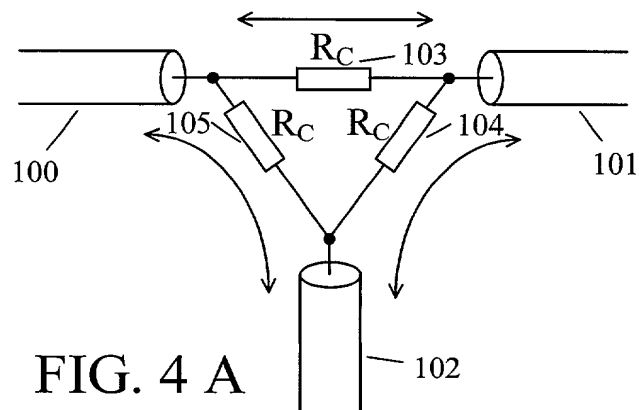


FIG. 3 E



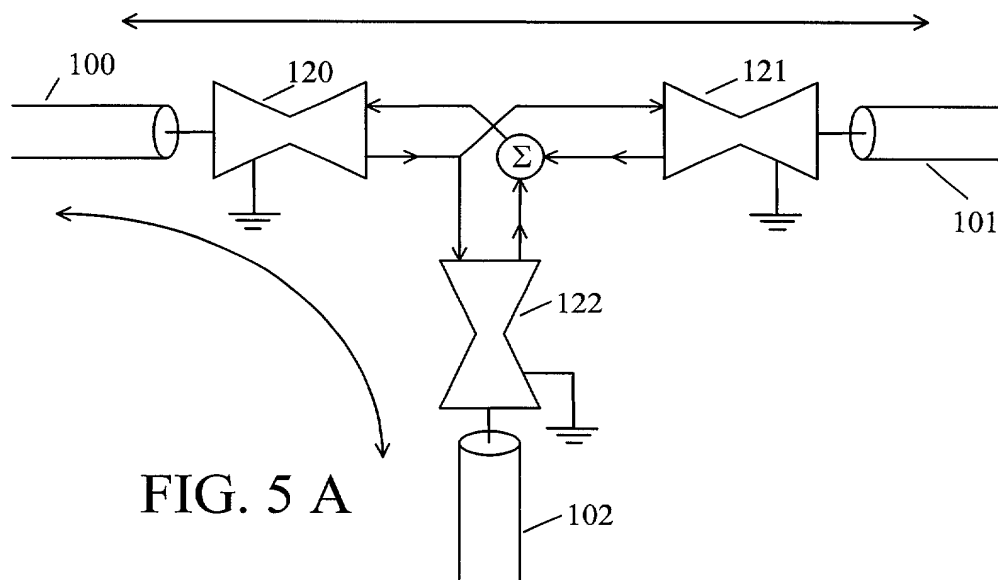


FIG. 5 A

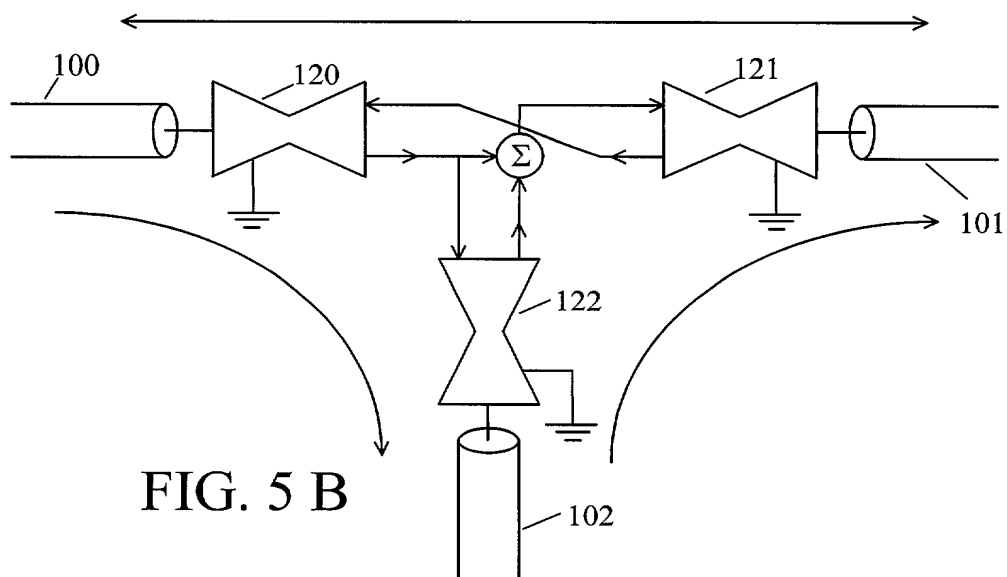
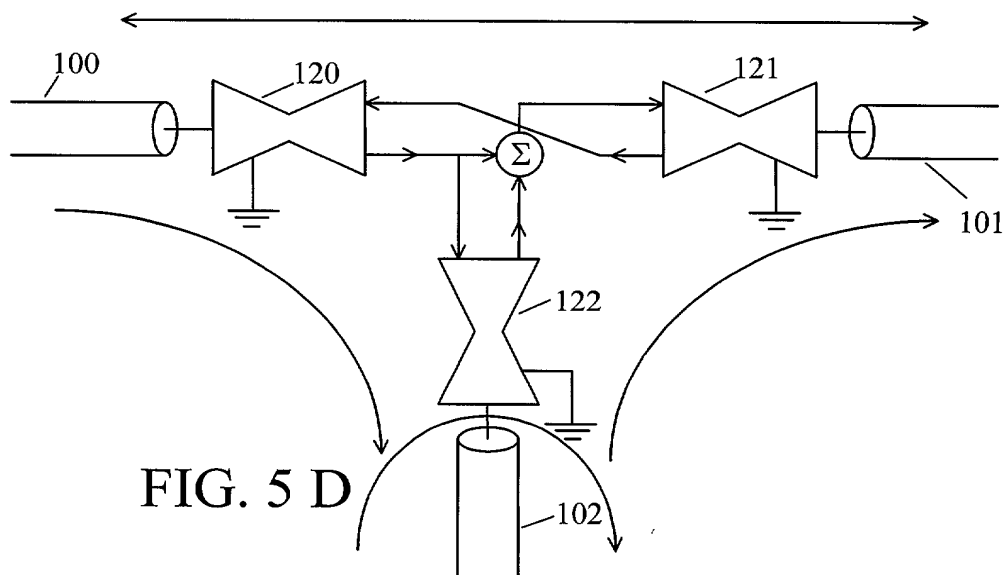
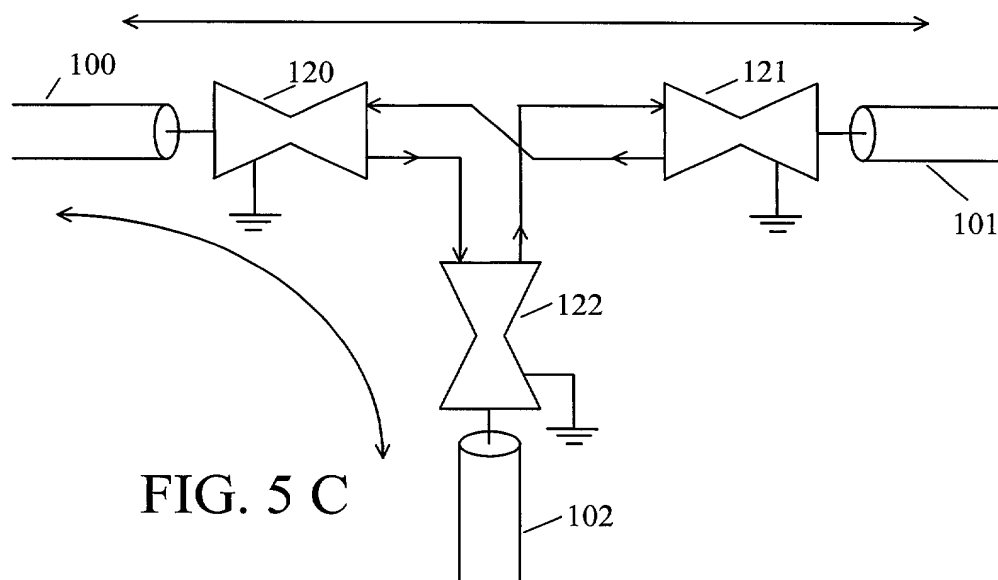


FIG. 5 B





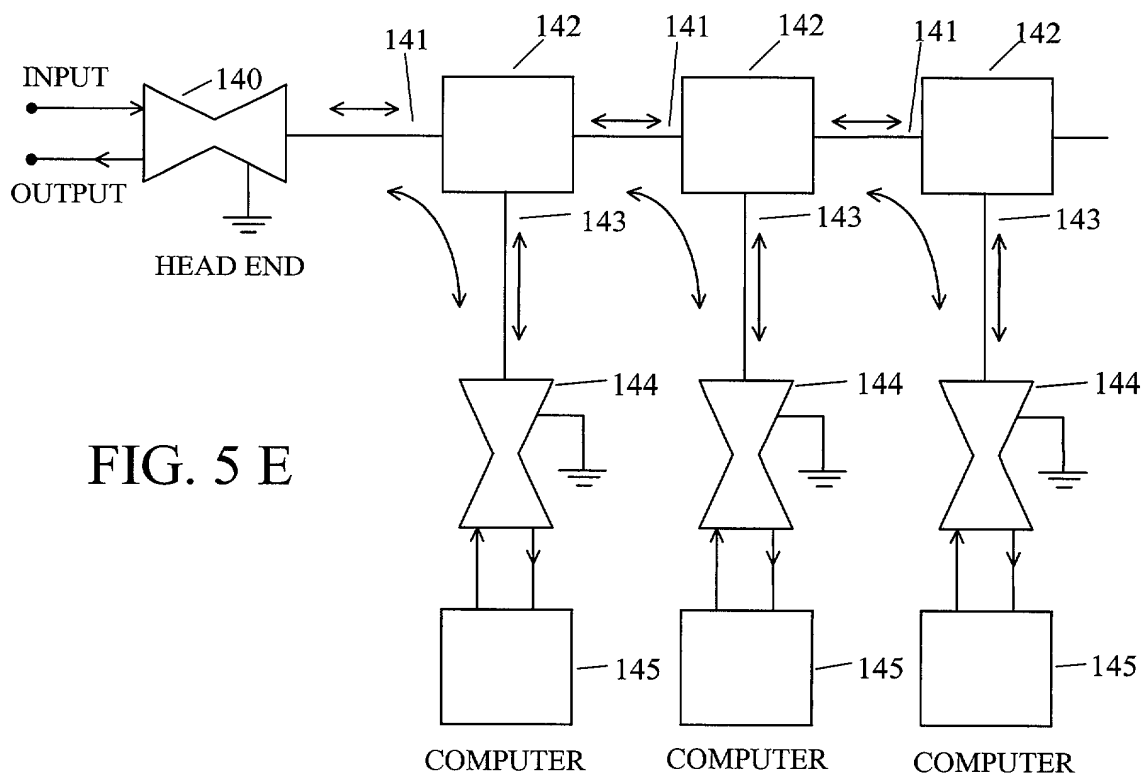
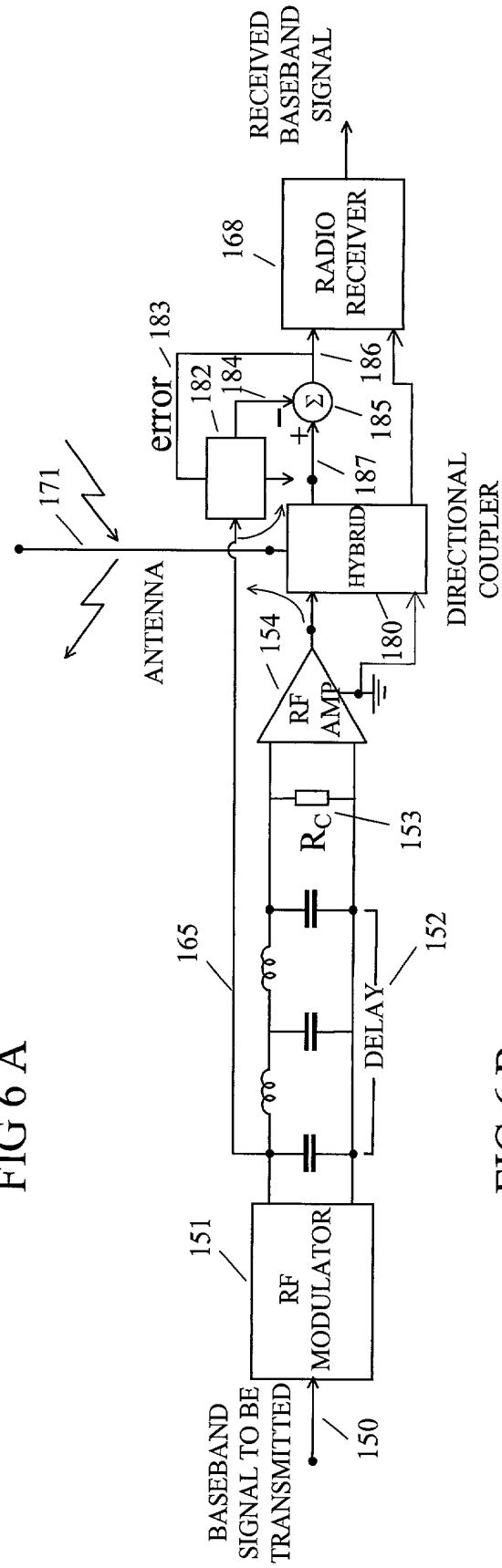
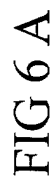
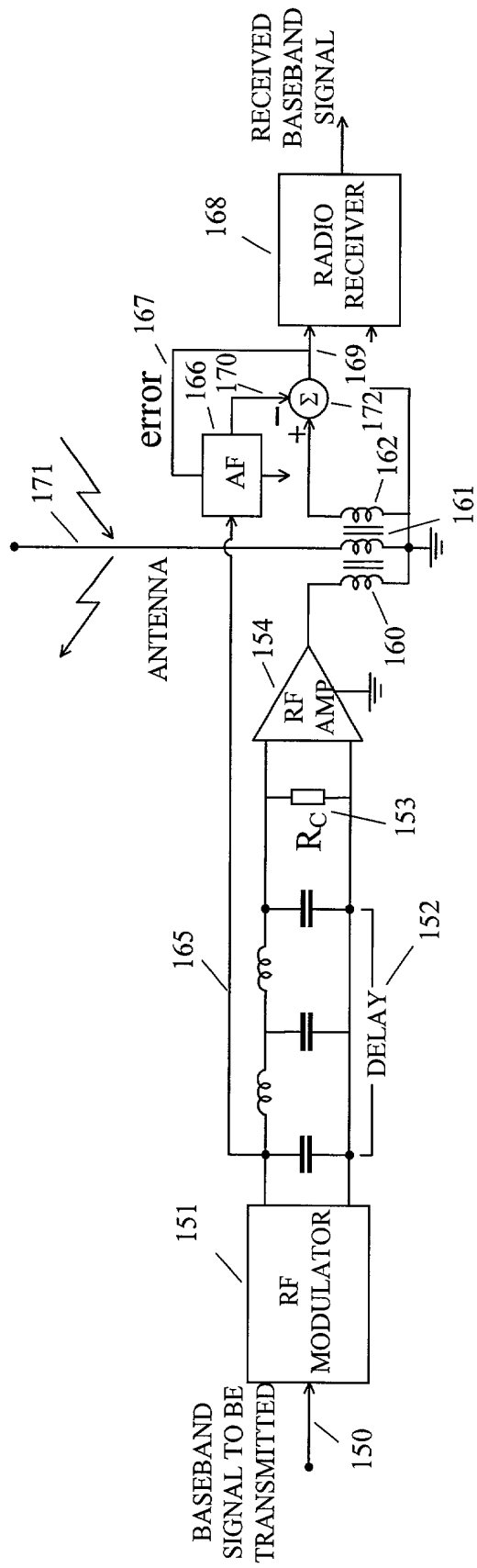


FIG. 5 E



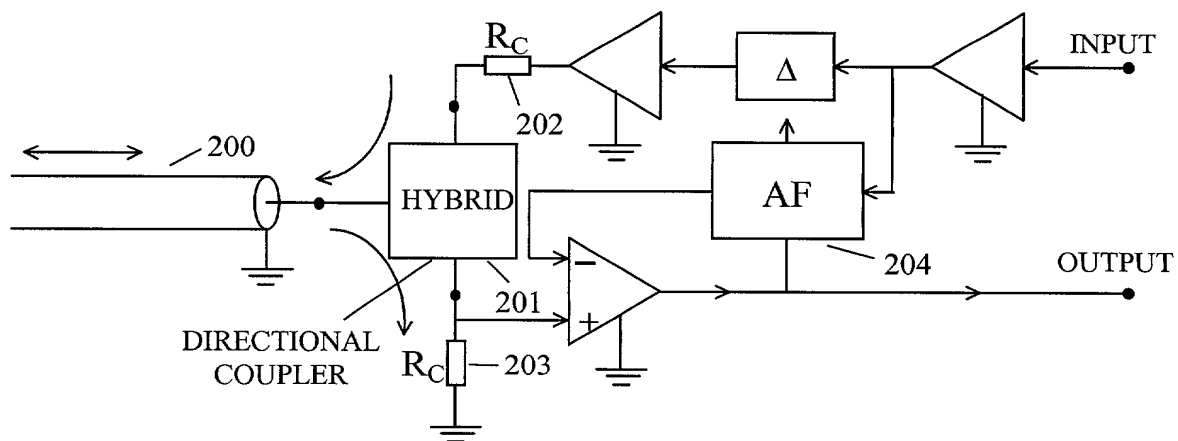


FIG. 7 A

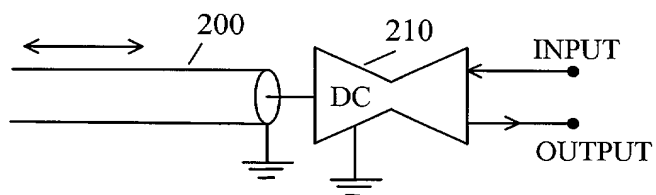


FIG. 7 B

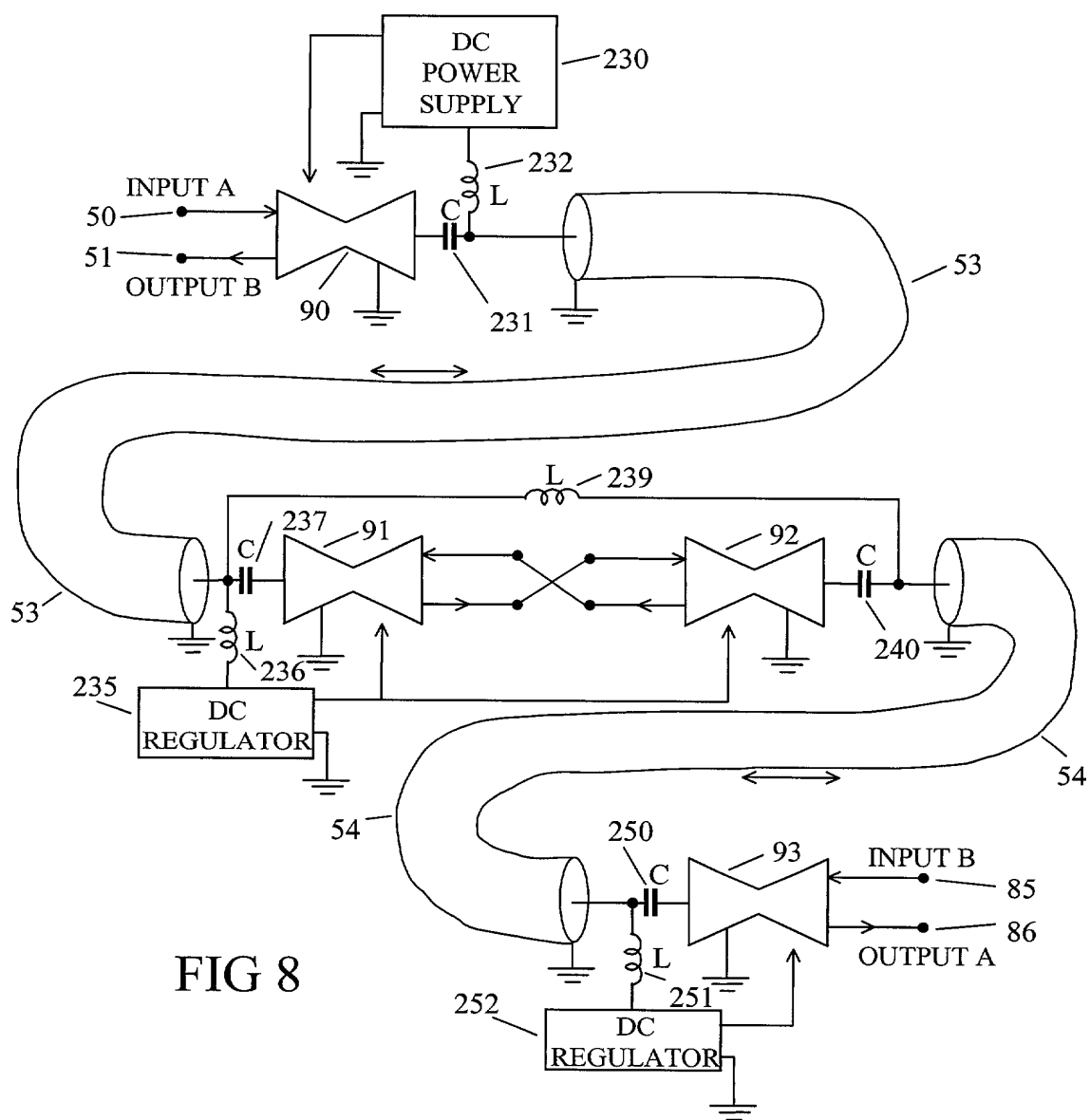


FIG 8

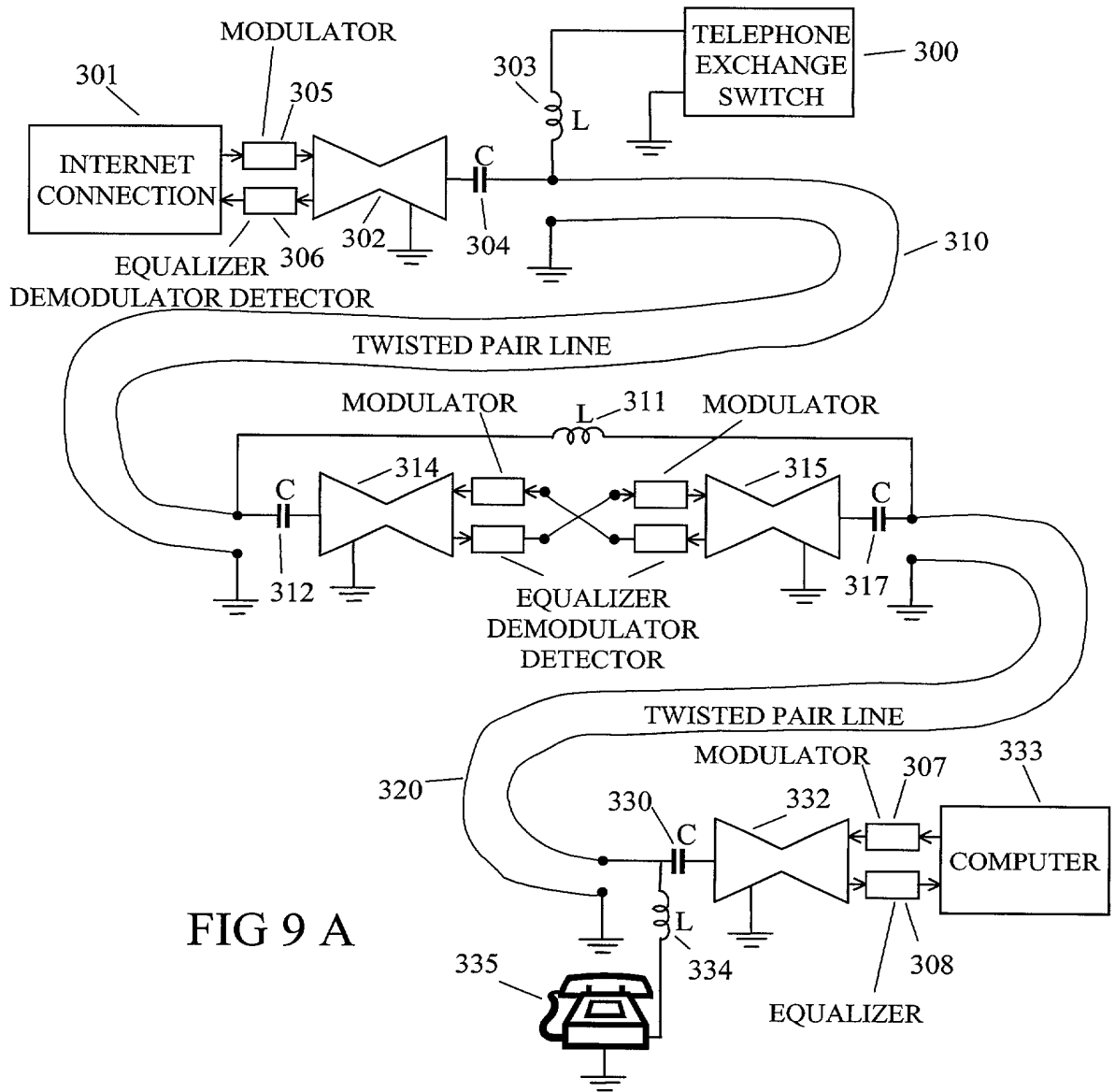


FIG 9 A

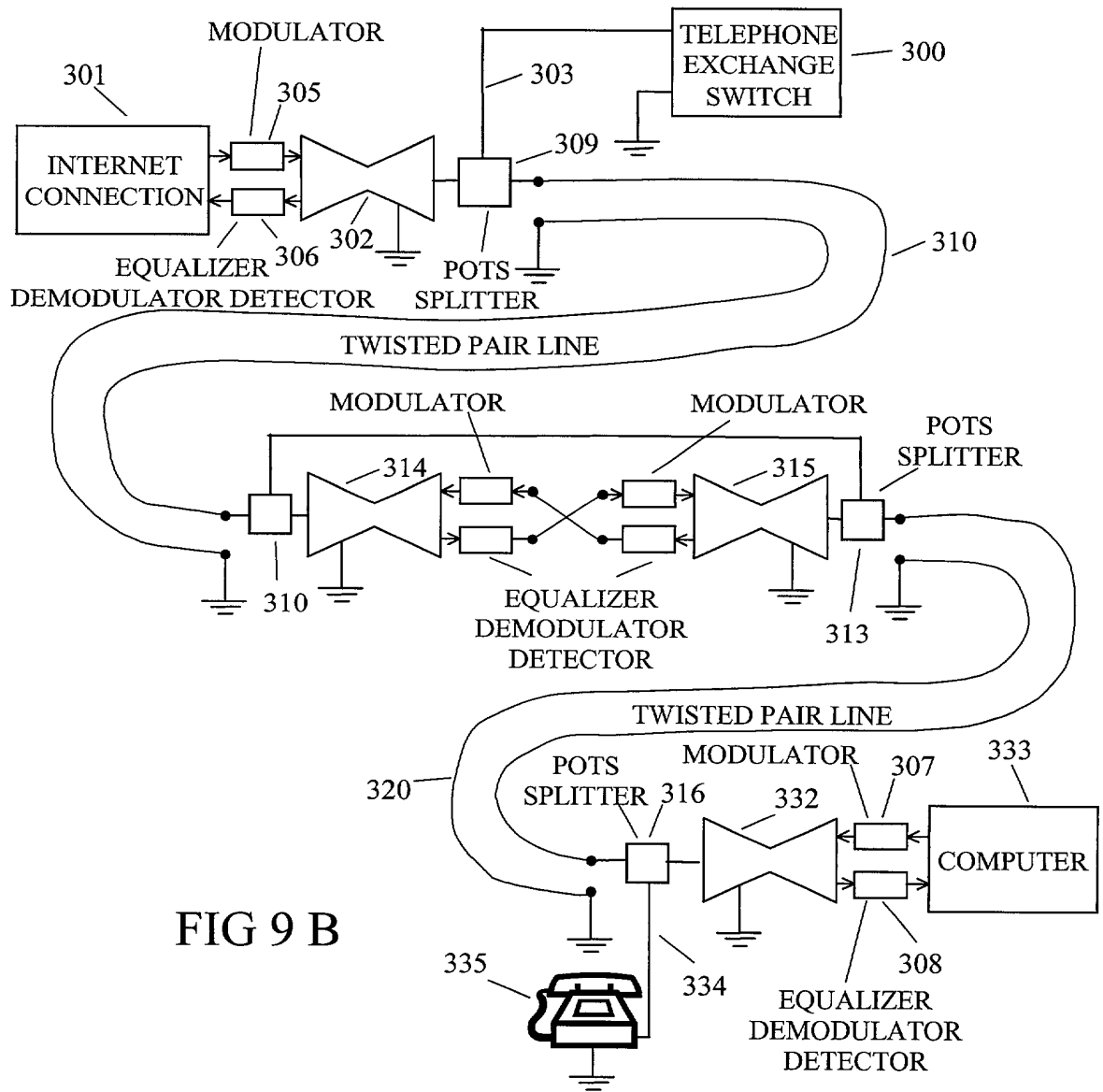


FIG 9 B

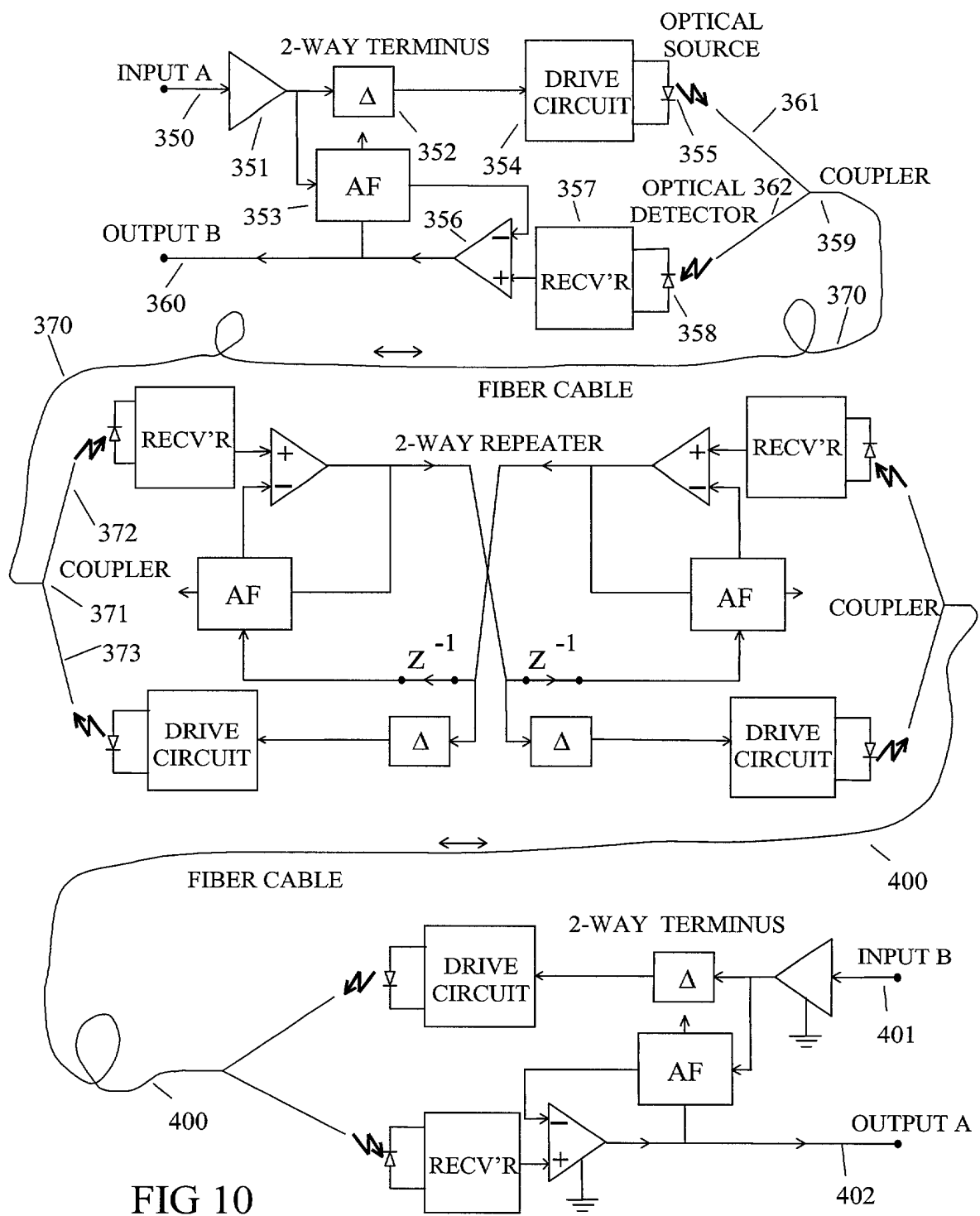


FIG 10



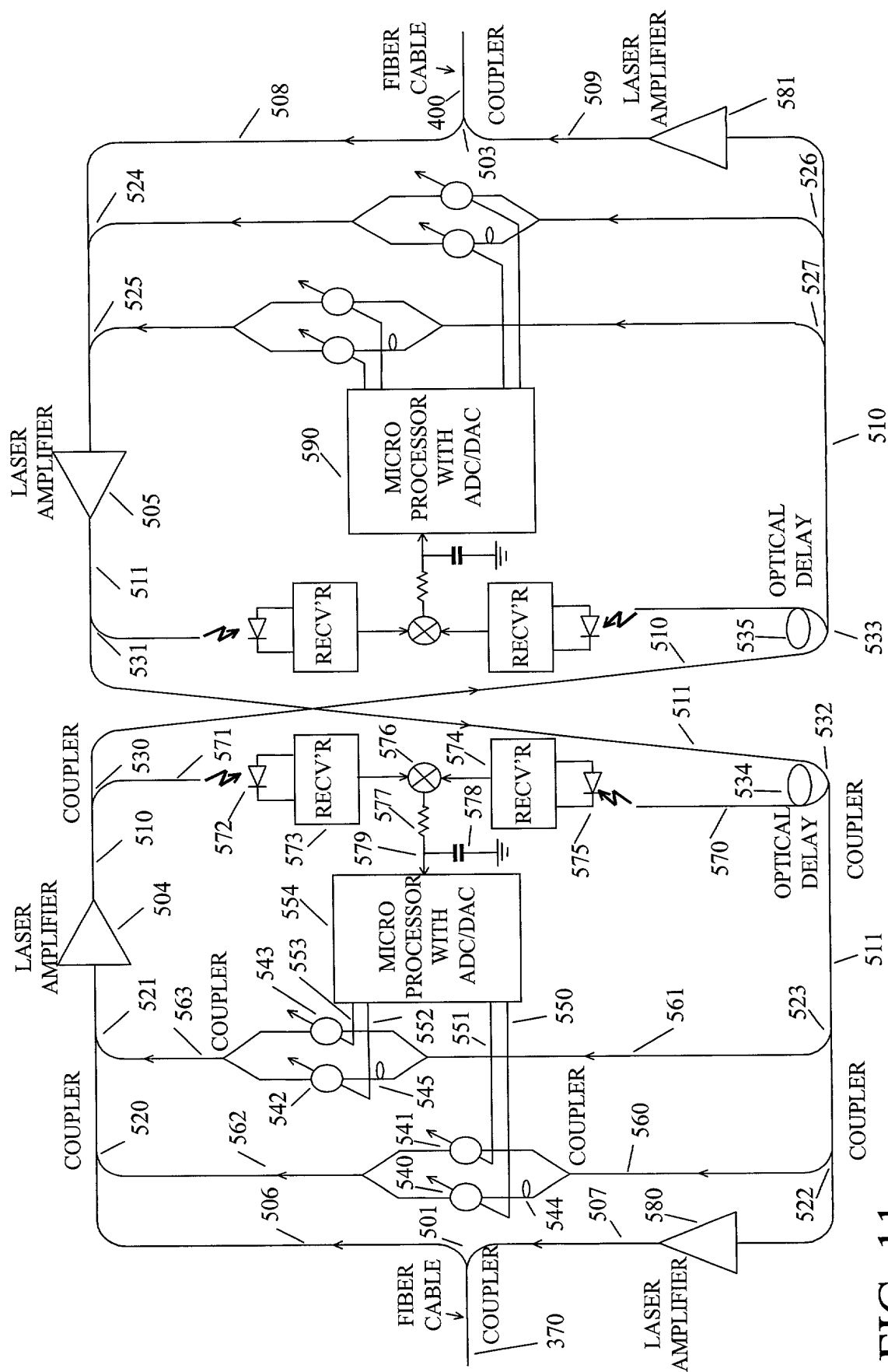
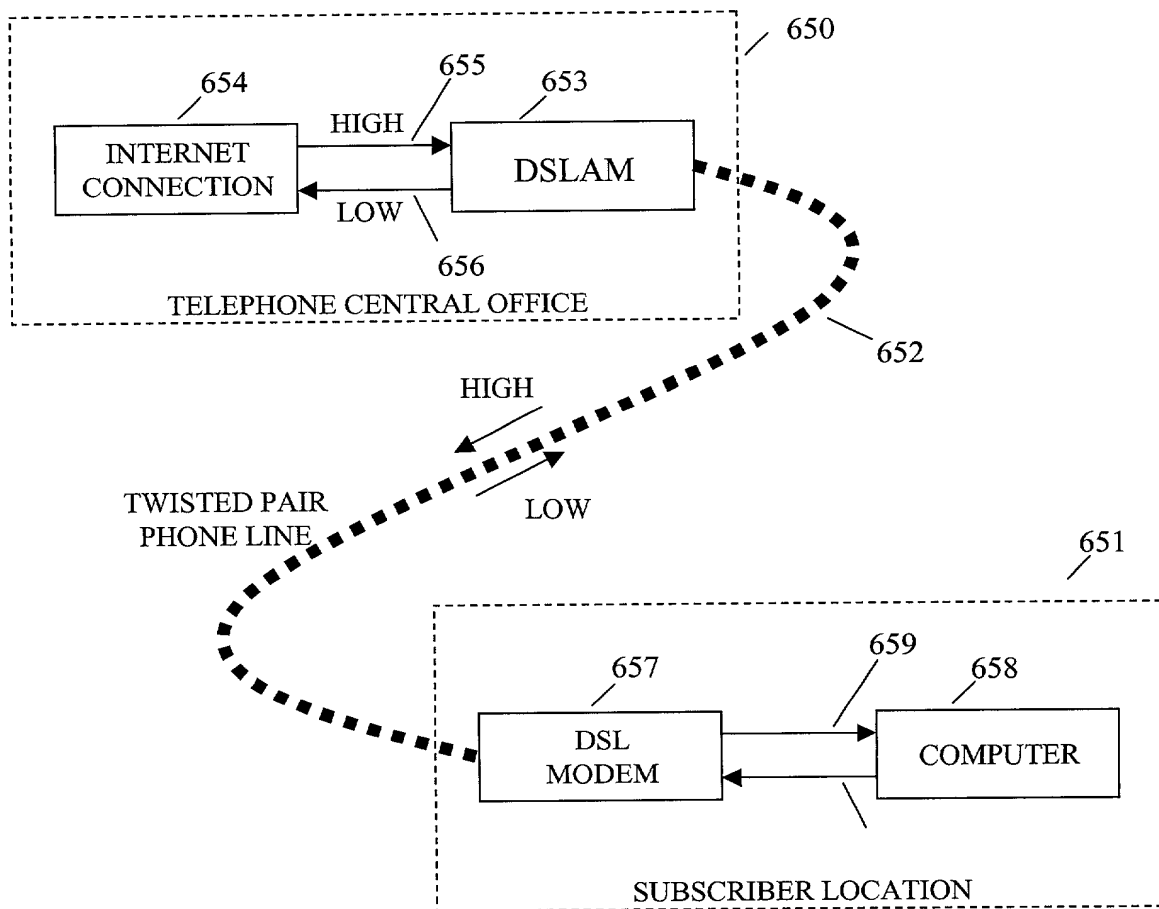


FIG. 11



HIGH = HIGH FREQUENCY, HIGH BANDWIDTH BITSTREAM  
 LOW = LOW FREQUENCY, LOW BANDWIDTH BITSTREAM

FIG. 12

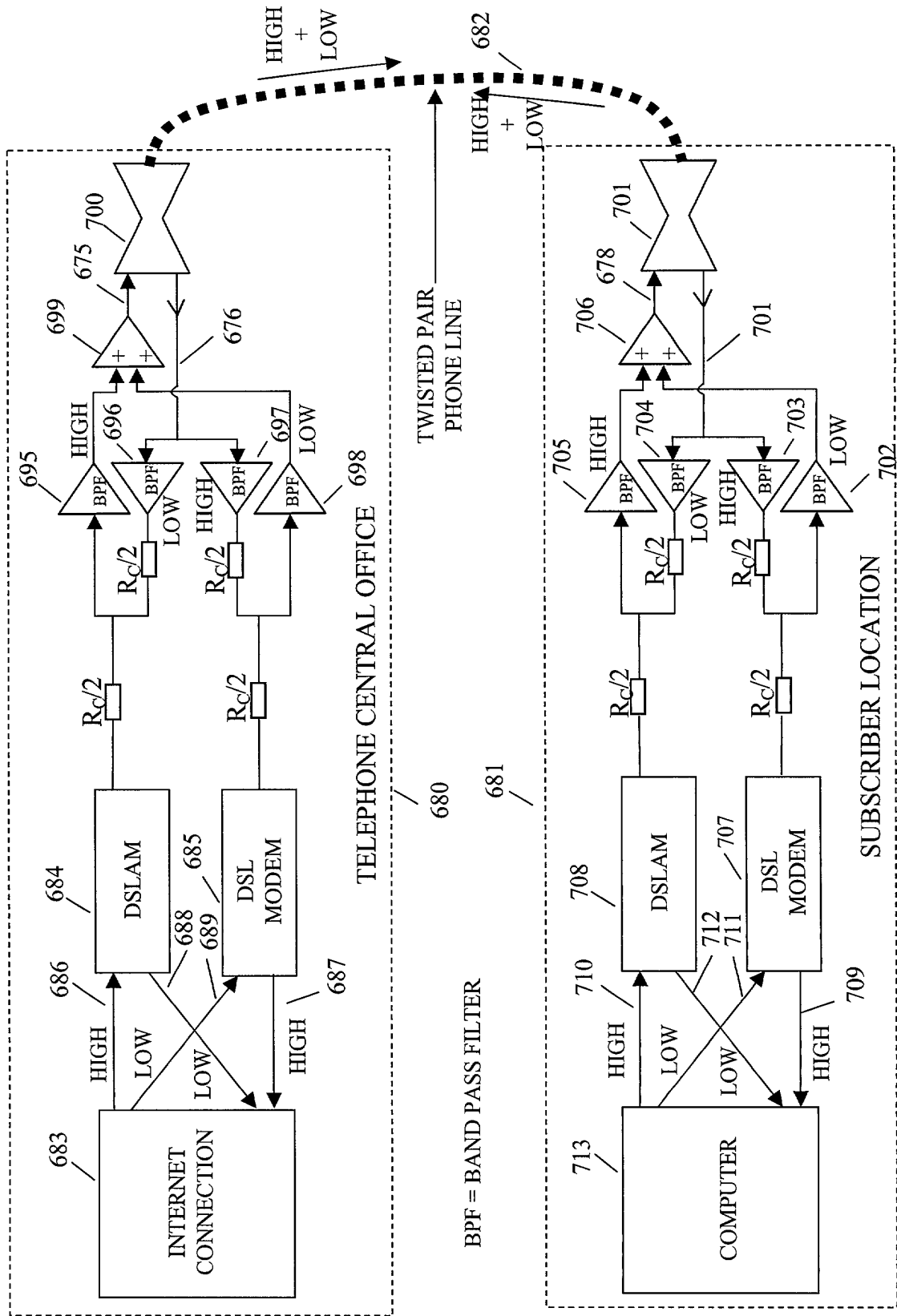


FIG. 13A

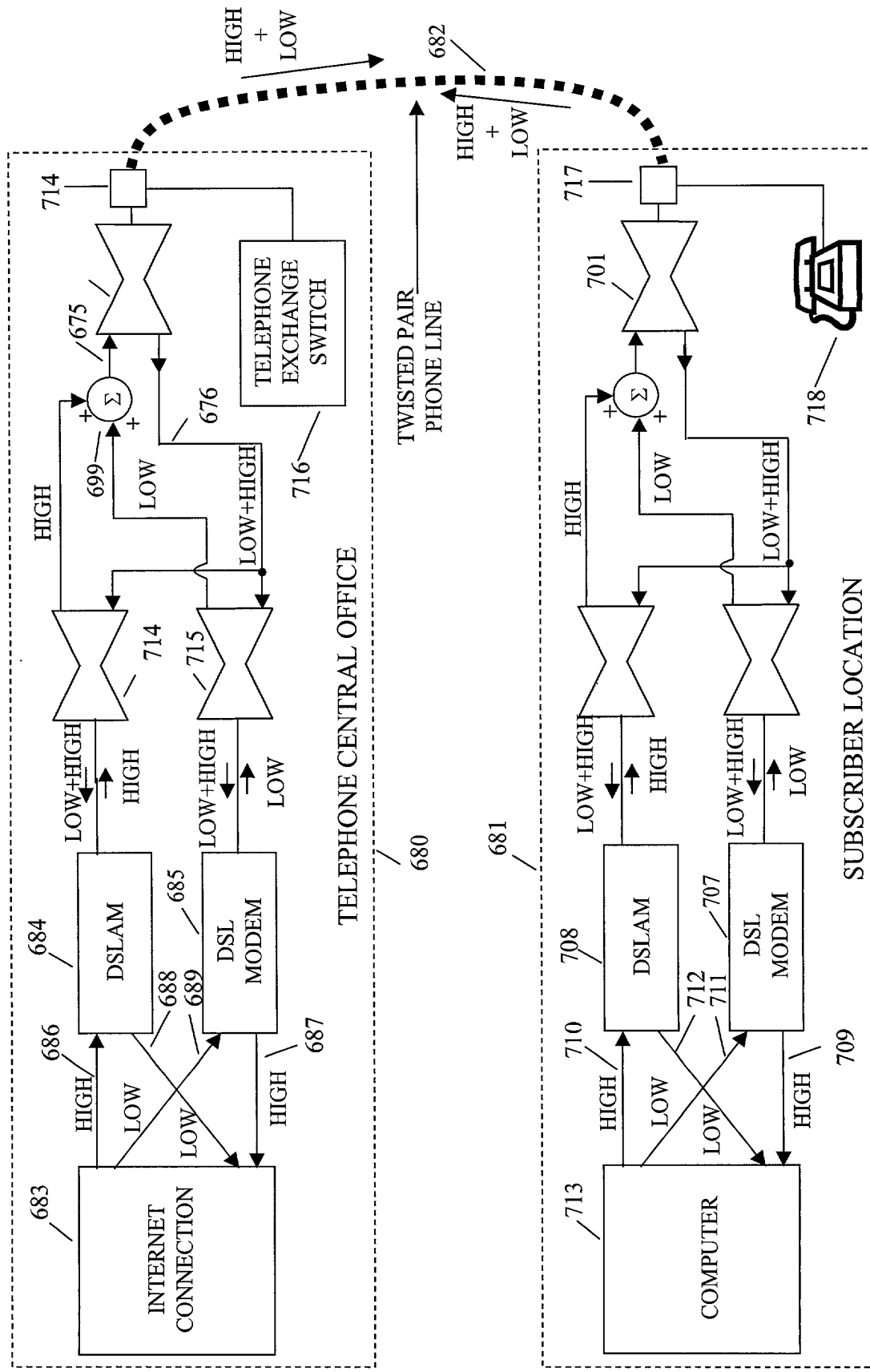


FIG. 13B

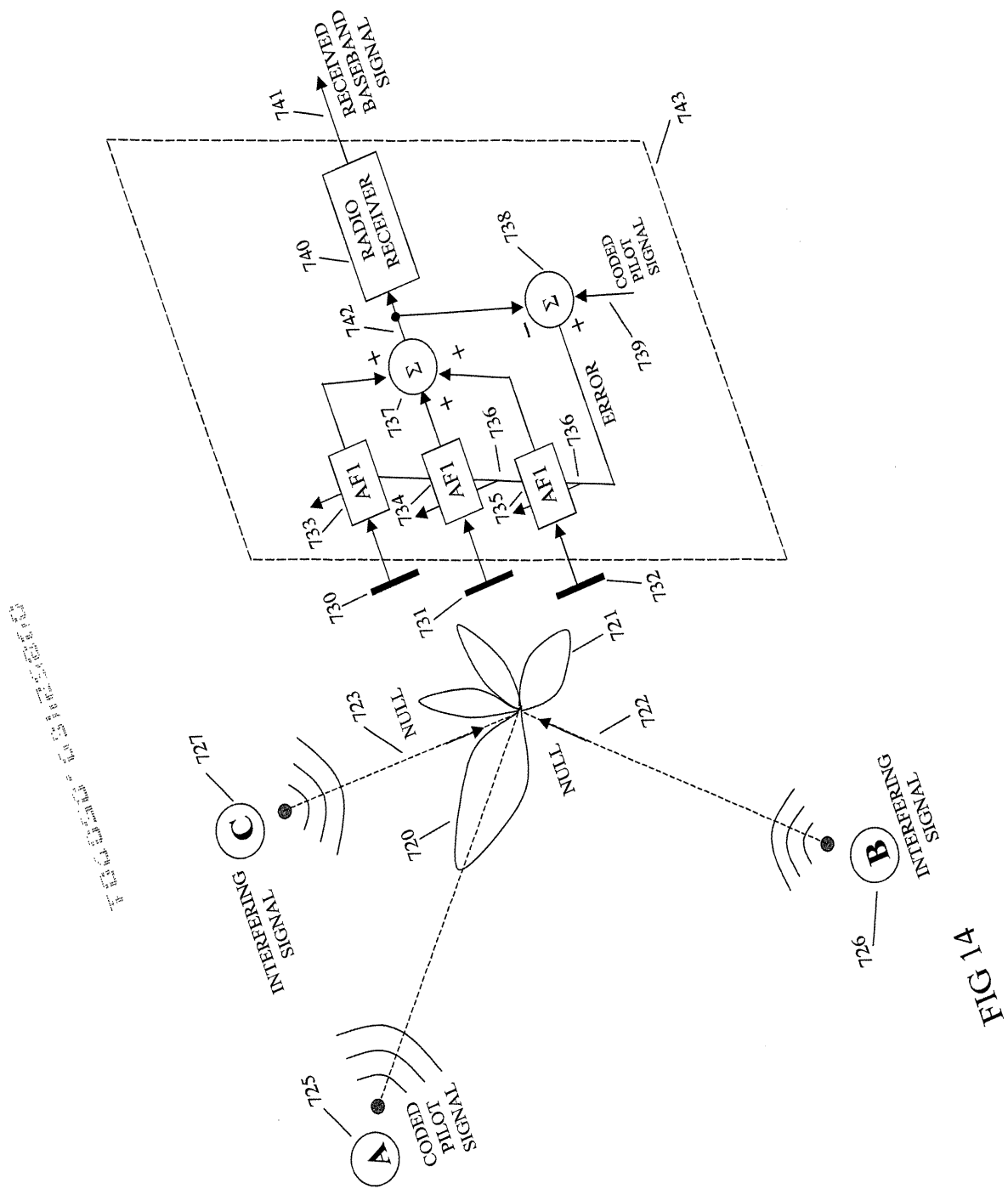


FIG. 15 is a block diagram of a 2-Way Adaptive Beamformer system, showing the internal structure of the beamformer and its interaction with an antenna array and external signals.

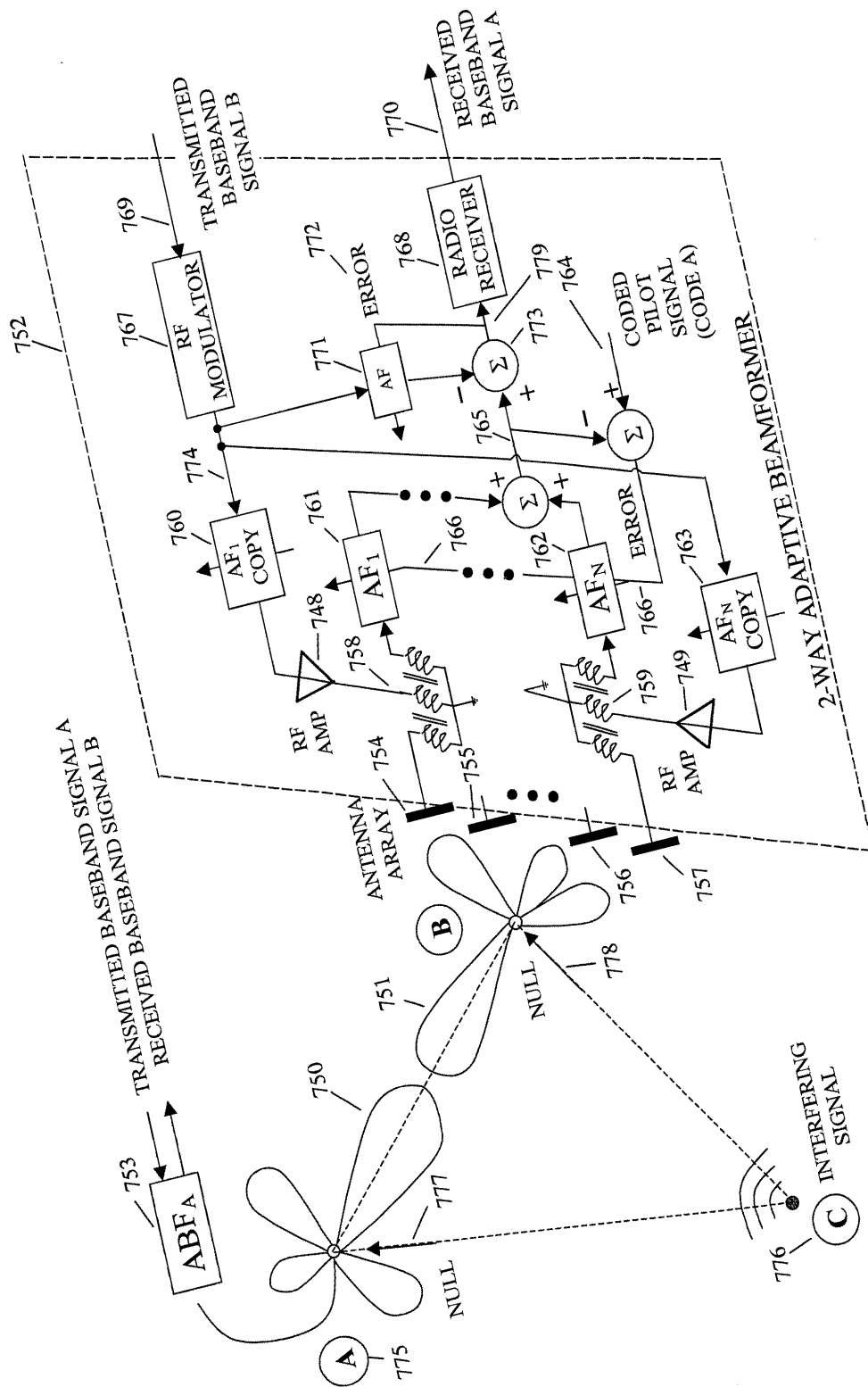


FIG. 15

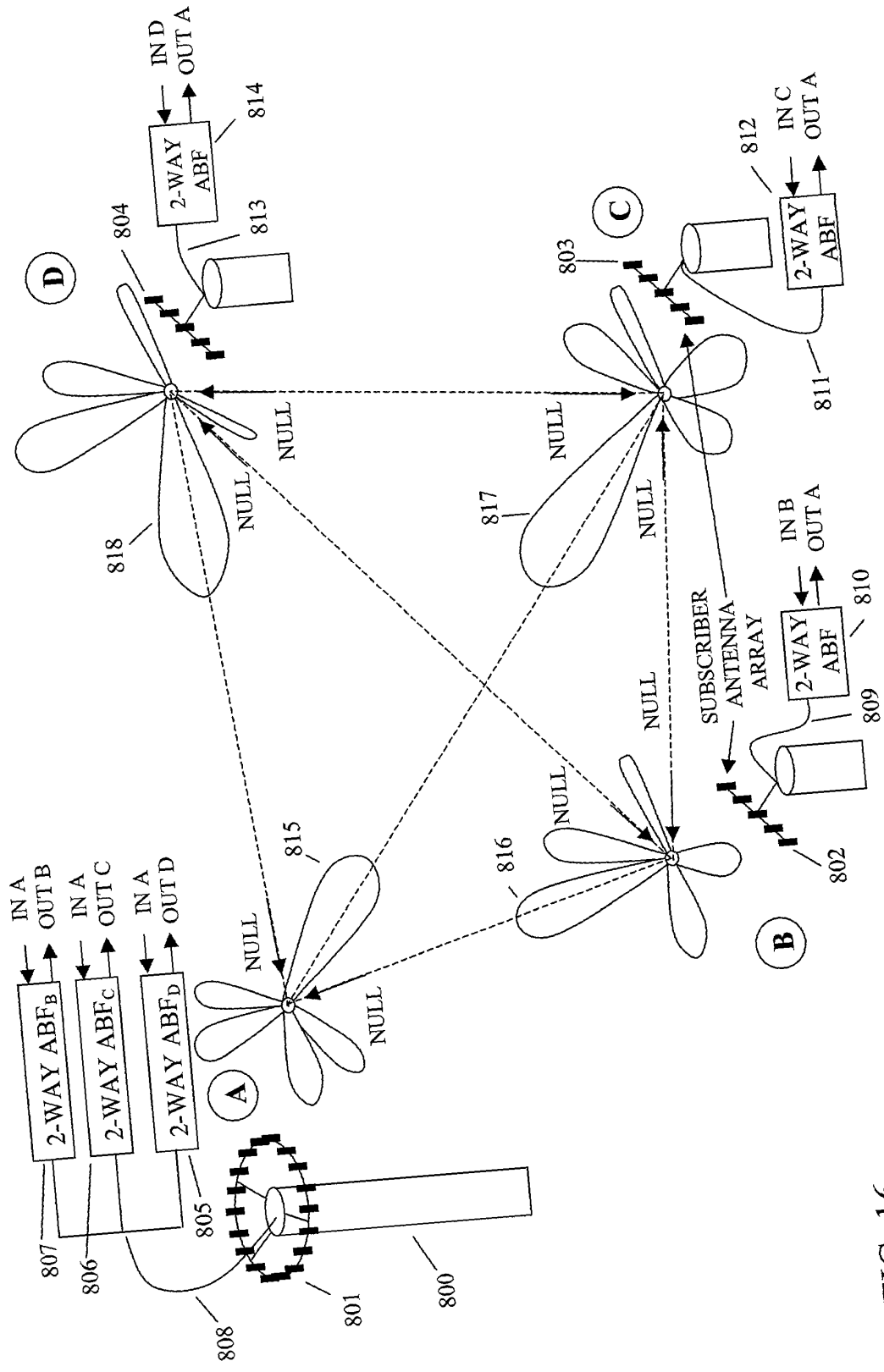


FIG. 16

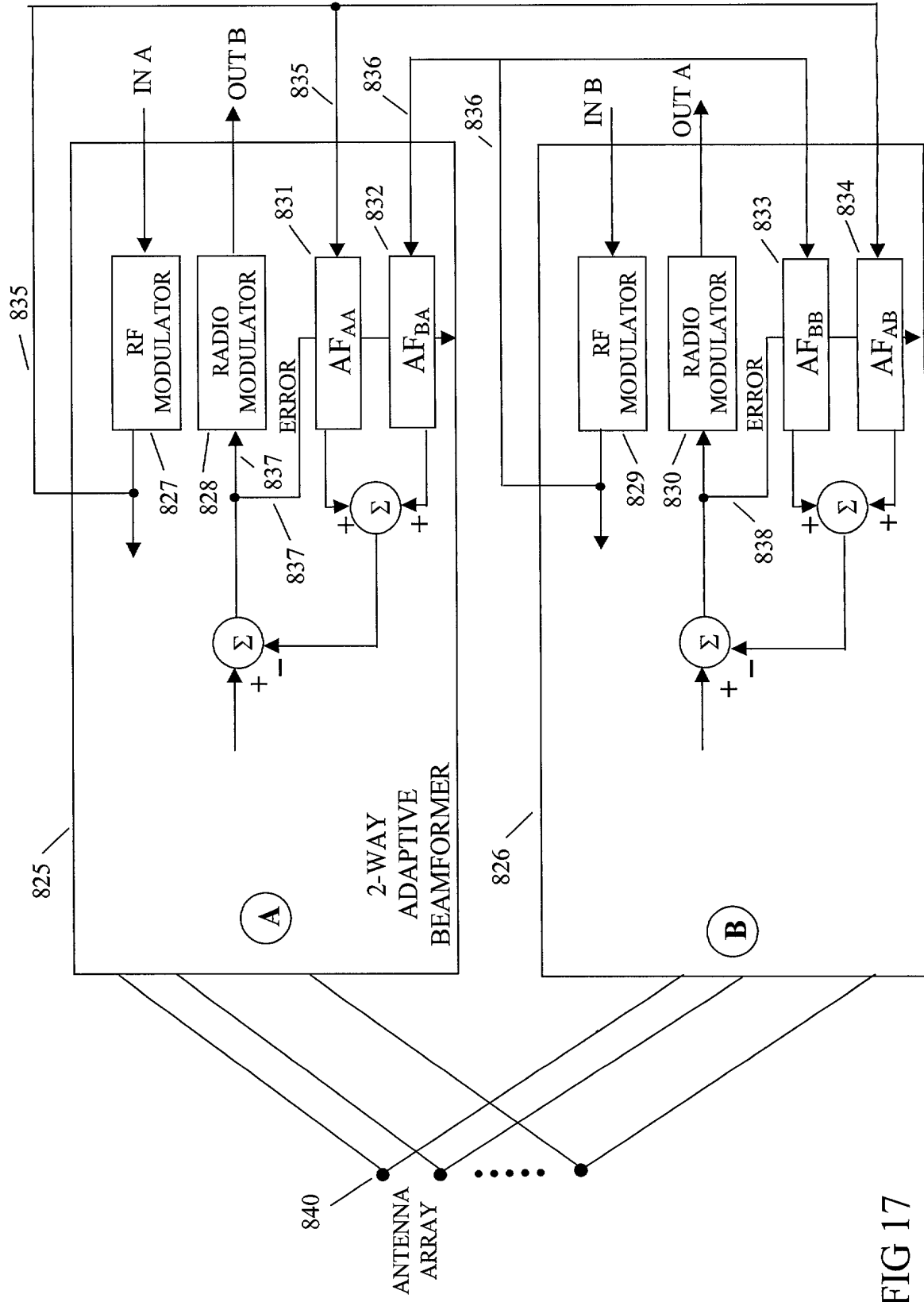


FIG 17